



# BEST AVAILABLE COPY

IN THE UNITED STATES PATENT AND TRADEMARK OFFICE

DOCKET NO.: TU9-98-010

In re Application of:  
**DENNING ET AL.**

Examiner: **B. BONZO**

Serial No.: **09/131,846**

Art Unit: **2113**

Filed: **JULY 24, 1998**

Confirmation No.: **8393**

For: **DATA PROCESSING METHOD  
AND SYSTEM FOR SIMULATION OF  
HARDWARE FAULTS UTILIZING A  
PCI BUS**

§  
§  
§  
§  
§  
§  
§  
§

## LETTER TO THE OFFICIAL DRAFTSMAN

**DRAWING REVIEW BRANCH**  
U. S. Patent and Trademark Office  
Alexandria, Virginia 22313-1450

Sir:

Submitted herewith are three (3) sheets of *replacement drawings* for the above-identified application, correcting the informalities noted by the Draftsperson regarding the original drawings filed with this patent application. No new matter has been added.

11/1/06  
(Date)

Respectfully submitted,

Andrew J. Dillon  
Registration No. 29,634  
DILLON & YUDELL L.L.P.  
8911 N. Capital of Texas Hwy., Suite 2110  
Austin, Texas 78759  
(512) 343.6116

ATTORNEY FOR APPLICANT(S)